

Amendments to the Claims

This listing of claims will replace all prior version, and listings, of claims in the application.

Listing of Claims:

1. (Currently amended) An interface definition comprising:
 - an electrical signal set definition, wherein the electrical signal set definition comprises eight (DPUSB) signals, one reserved signal, a write protect signal and a card detect signal and wherein the eight DPUSB signals comprise peripheral differential data lines and host differential data lines;
 - a mechanical and electrical definition for a memory card;
 - an associated memory card signal and mechanical specification for a memory card;and
 - a mechanical definition for cables compatible with an interface socket.
2. (Cancelled)
3. (Currently amended) The interface definition of claim [2] 1 wherein pin assignments are mapped to digital pins using DPUSB signals.
4. (Currently amended) An interface socket comprising:
 - a signal set that defines a peripheral port, a host port, a write-protect signal, wherein the electrical signal set definition comprises eight (DPUSB) signals, one reserved signal, a write protect signal and a card detect signal and wherein the eight DPUSB signals comprises peripheral

differential data lines and host differential data lines; and

a form factor that is equivalent to a secure digital card.

5. (Cancelled)

6. (Currently amended) The interface socket of claim [5] 4 wherein pin assignments are mapped to digital pins using DPUSB signals.

7. (Currently amended) The interface socket of claim [5] 4 wherein the socket can be interfaced to both a USB host device and a USB peripheral device.